ABSTRACT OF THE DISCLOSURE

The invention relates to an image processing apparatus which has a coding function of image data and inputs and outputs an image and a method for such an apparatus, and has an object to simultaneously perform coding of the image data in parallel in both a CPU and a coder/decoder. A system bus bridge (SBB) is a multichannel bidirectional bus bridge and provides a mutual connection between a B bus (I/O bus), a G bus (graphic bus), an SC bus (processor bus), and an MC bus (local bus) by using a crossbar switch. The connections of two systems can be simultaneously established by the crossbar switch. A high speed data transfer of high parallel performance can be realized among a CPU core, a CODEC, and a DRAM.